



P ————— Precise
L ————— Low-Noise
D ————— Durable
R ————— Reliable
O ————— Outperforming



FEATURES

- DIELECTRIC RESONATOR
- INTERNAL VOLTAGE REGULATOR
- PHASE LOCK INDICATOR ALARM
- LOW PHASE NOISE
- MIC AND SMT FABRICATION
- LOW MICROPHONICS
- LOW POWER CONSUMPTION
- UP TO +25 dBm OUTPUT POWER
- AVAILABLE FROM 4 - 23 GHz
- OPERATING RANGE -50° TO +95°C

APPLICATION

- SATELLITE COMMUNICATIONS
- CABLE TV LINKS (CATV)
- LOCAL AREA NETWORKS (LAN)
- GLOBAL POSITIONING SYSTEMS (GPS)
- TEST EQUIPMENT
- TRAFFIC SIGN POSTS
- LMDS
- TRANSMITTER & RECEIVERS
- DIGITAL RADIOS
- MISSILE GUIDANCE

DESCRIPTION

PLO-2000 series Phase Locked Dielectric Resonator Oscillator (PLDRO) utilizes state of the art MIC and SMT to provide crystal stability at microwave frequencies up to 23 GHz. The low profile and rugged construction provide excellent durability against harsh environmental conditions.

PLO-2000 series oscillator is designed using GaAs FET or BJT amplifier with series feedback at source and Dielectric Resonator at the gate. High gain, low-noise GaAs FETs/BJTs are biased positively or negatively at the gate to ensure minimum phase-noise. The device is carefully matched for maximum power, minimum phase-noise and Voltage standing-Ratio (VSWR). The oscillator is matched for maximum temperature stability and optimum negative resistance.

PLO-2000 series oscillator is buffered by cascaded low-noise driver and power amplifiers for minimum load pulling, maximum isolation and power. GaAs FET/BJT devices are directly attached to gold plated Kovar carriers to minimize shear effect and maximize heat sinking. Kovar carriers are mounted to the chassis to provide an efficient thermal junction and a stable structure for reduction of microphonics. To ensure oscillator stability over the full temperature range, the tuning elements are precisely designed and positioned to compensate for temperature drift by a factor of three.

PLO-2000 series proprietary Phase lock loop circuitry is surface mounted on thick film Alumina substrate. The reference frequency is multiplied and sampled to output frequency. Produced error voltage due to frequency drift is sensed by a Wein-Bridge Oscillator to provide the necessary sweep voltage to an ultra Hi-Q tuning varactor diode for the purpose of compensation and phase locking. The unique construction of phase lock loop sub-assembly provides excellent temperature stability and minimum solder joints for maximum reliability.

PLO-2000 series is internally voltage regulated to avoid reverse bias, frequency pushing, bias modulation and voltage transients. A phase lock indicator alarm of open TTL type is provided as a feature. The PLO-2000 series are externally reference locked and factory tuned to specified frequency. Mechanical frequency adjustment is provided for optimum phase voltage setting.

SPECIFICATIONS

Model Number	PLO-2000-XX.XX (Where XX.XX is freq. in GHz)
Single Frequency	4.00 to 23.00 GHz
Mechanical Tuning Range	100 MHz
Power Output	+13 dBm, up to +25 dBm Optional
Load VSWR, Maximum	2.0 : 1.0
Power Requirements	+12, +15 VDC, 270 mA
Reference Input Frequency	50 to 200 MHz
Reference Power Input	Nominal +/- 6 dBm
Reference Power Input Range	-10 to +10 dBm
Frequency Stability	Same as Reference
Phase Noise	See Phase Noise Envelope (Fig. A)
Spurious	-80 dBc
Harmonics	-25 dBc
Alarm	TTL, Open Collector
Operating Temperature	-50° to +95°C Optional
Storage Temperature	-55° to +125°C
Connectors	SMA, Female
Size	2.25" x 2.25" x .67"
Finish	Nickel

PHASE NOISE ENVELOPE

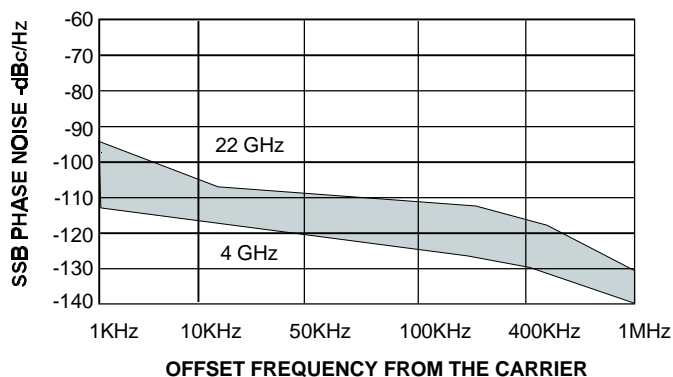
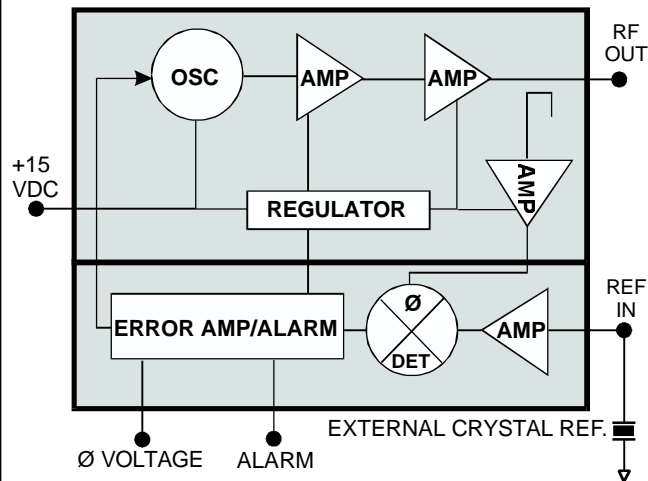
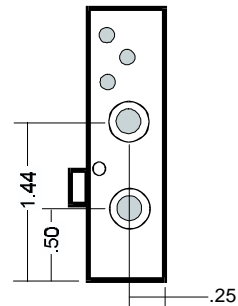
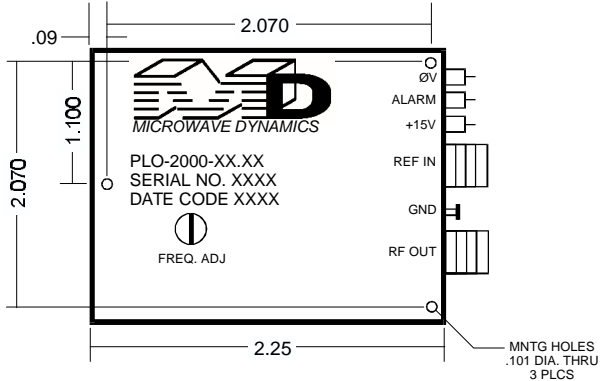
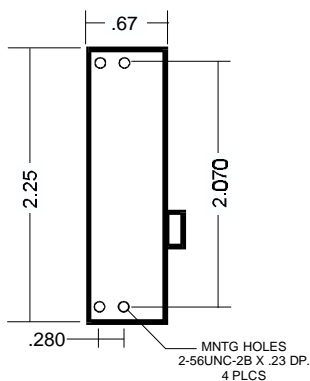


FIGURE A

BLOCK DIAGRAM



OUTLINE DRAWING



NOTE: Drawing not to scale

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