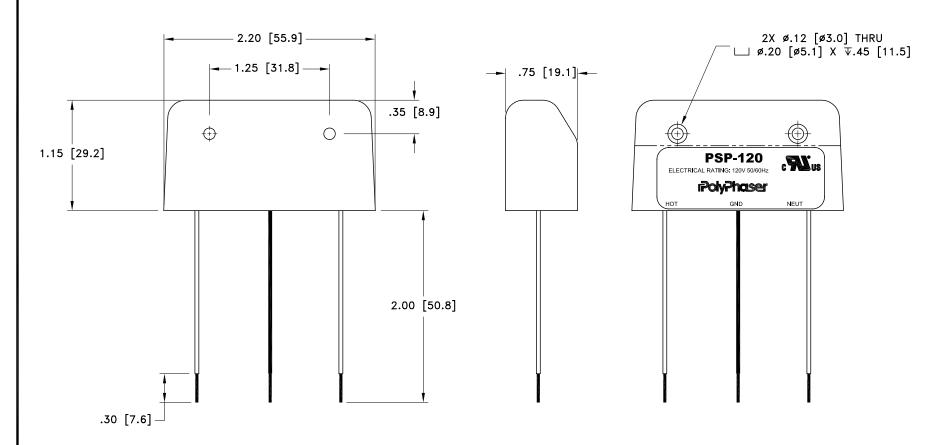
ALL DESIGN, OPERATIVE AND PROCESS DATA PERTAINING TO THE ARTICLE SHOWN ON THIS SHEET IS THE PROPERTY OF POLYPHASER CORPORATION. THE INFORMATION IS NOT TO BE COPIED, REPRODUCED, REVALUED OR APPROPRIATED BY OTHERS WITHOUT THE EXPRESS WRITTEN, CONSENT OF COUNTRY CORPORATION.

REVISIONS				
REV LTR	DATE	ENG	MKTG	Q.A.
Α	07/01/03 வ	LC	SD	LJ



MAXIMUM CHARACTERISTICS

SURGE

35kA IEC 61000-4-5 8/20µs WAVEFORM

TURN-ON:

±240Vdc LINE TO LINE

±360 TO 700Vdc LINE TO GROUND

SUPPRESSION:

400V LINE TO LINE

400V LINE TO GROUND

TURN-ON TIME:

<2ns L TO L, 10ns L TO G FOR 2kV/ns

ALL REVISIONS TO THIS DOCUMENT MUST BE UL APPROVED.

CUSTOMER APPROVAL: DATE:

ALL DIMENSIONS SHOWN ABOVE ARE FOR REFERENCE ONLY.

I. BENNETT	DATE 02/28/03	r PolyPhaser	
MECH ENGINEER J. JONES	DATE 03/26/03	1 2	
ELEC ENGINEER L. CHANG	DATE 03/26/03	PSP-120	
MARKETING S. DOTTER	DATE 03/26/03	CUSTOMER PRINT	
QUALITY DEPT R. MATHEUS	DATE 03/26/03	CAGE CODE FILE NAME	